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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/772,360	02/06/2004	Hitoshi Furukawa	00862.023449.	5049
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EXAMINER				
MILLA, MARK R				
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/772,360

Applicant(s)

FURUKAWA, HITOSHI

Examiner

Mark R. Milia

Art Unit

2625

Period for Reply -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 November 2010.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1, 3, 4, 8, 10 and 11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 3, 4, 8, 10 and 11 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-945)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 11/30/10 has been entered. Currently, claims 1, 3-4, 8, and 10-11 are pending.

Claim Rejections - 35 USC § 103

2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
3. Claims 1, 3-4, 8, and 10-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hanyu (US 7,131,124) in view of Muto (US 2002/0018665), Ootani (US 2003/0093612), and Kazama (US 5,822,522).

Regarding claim 1, Hanyu discloses a data transfer method between a first controller which controls an engine section for forming an image and a second controller which transmits image data to the first controller, wherein the engine section includes a nonvolatile memory, wherein in an image forming operation mode of forming an image

with the engine section, the method comprises: notifying the second controller of a condition change of the engine section by the first controller, (see Fig. 3 and column 8 line 59-column 9 line 60, input section **502** determines when an instruction to download a control program from a host computer is input via a control panel **303** and if so, the engine controller **301** terminates all operations of the printer engine and clears the flash EEPROM **301b** as to ready the flash EEPROM for download on the new control program. After the engine controller **301** completes the above tasks, it sends a demand for transmission, which is also seen as a notification that the engine controller is ready for control program download, to the printer controller **300**, which in turn acquires the control program from the host and then transfers the control program frame by frame to the engine controller **301** for writing of the program into memory, flash EEPROM **301b**), and transmitting a condition request instruction by the second controller to the first controller via a data signal line, in response to the notification of the condition change (see column 9 lines 4-10, in response to a download instruction the engine controller **301** terminates the operation of each component of the printer engine **302**), and wherein in a rewrite mode of rewriting the nonvolatile memory, the method comprises: transmitting a rewrite instruction by the second controller to the first controller (see Fig. 3, column 4 lines 47-49, column 7 lines 30-34 and 48-54, column 8 lines 59-67, and column 9 lines 3-50, synchronization is established after an instruction to download a control program is detected, then the engine controller requests transmission of a downloaded program from the printer controller, which in turn receives the downloaded program from the host computer, thereby instruction to retrieve the downloaded control

program is initiated and in response the control program is transferred to the engine controller and the flash memory is rewritten with the new program), and transmitting rewrite data by the second controller to the first controller via the data signal line, in response to the rewrite instruction, (see Fig. 3, column 8 lines 1-8, and column 9 lines 51-65).

Hanyu does not disclose expressly notifying a condition change by changing a signal level of a report signal line from a first level to a second level, transmitting a condition request instruction by the second controller to the first controller via a data signal line in response to the notifying of the condition change, while the signal level of the report signal line is at the second level, transmitting condition information by the first controller to the second controller via the data signal line in response to the condition request instruction, after the signal level of the report signal line is returned to the first level, and in a rewrite mode transmitting an instruction by the second controller to the first controller via the data signal line, while the signal level of the report signal line is at the first level, transmitting data by the second controller to the first controller via the data signal line in response to the rewrite instruction, while the signal level of the report signal line is at the first level, notifying the second controller that the first controller is not ready for reception of the rewrite instruction or the rewrite data by changing the signal level of the report signal line from the first level to the second level, and maintaining the report signal line at the second level while the data of the nonvolatile memory is rewritten by the first controller by the rewrite data transmitted from the second controller, and notifying the second controller that the first controller is ready for reception of the

rewrite data by changing the signal level of the report signal line from the second level to the first level, after the data of the nonvolatile memory has been rewritten by the first controller by the rewrite data transmitted from the second controller, wherein, in the rewrite mode, the data of the nonvolatile memory is repeatedly rewritten by the first controller by the rewrite data transmitted from the second controller by alternately changing the signal level of the report signal line to the first level or to the second level, and wherein there is a determination that an error has occurred if the signal level of the report signal line is not changed from the second level to the first level within a time period including time for communication between the first and second controllers and time for rewriting the data of the nonvolatile memory, and if it is determined that the error has occurred, the rewrite data is transmitted to the first controller by the second controller again.

Muto discloses notifying a condition change by changing a signal level of a report signal line from a first level to a second level (see paragraphs 58 and 69, signal line /CCRT is used to inform status change in the printer to print controller **213** and shifts from a high level to a low level when a change is detected in the printer), transmitting a condition request instruction by the second controller to the first controller via a data signal line in response to the notifying of the condition change, while the signal level of the report signal line is at the second level (see paragraph 69, signal /CCRT stays at a low level while the printer controller **213** issues a command requesting the status), transmitting condition information by the first controller to the second controller via the data signal line in response to the condition request instruction, after the signal level of

the report signal line is returned to the first level (see paragraphs 68, 71, and 73, after the change has cleared /CCRT returns to a high level and then the print controller **213**).

Ootani discloses in a rewrite mode transmitting an instruction by the second controller to the first controller via the data signal line, while the signal level of the report signal line is at the first level (see Fig. 5 and paragraphs 44-47, 53, and 56-60, signal line RYIBY is in a ready or H level while executing instructions or commands, such as an instruction for rewrite mode shown by signal line IWE shifting to L), transmitting rewrite data by the second controller to the first controller via the data signal line in response to the rewrite instruction, while the signal level of the report signal line is at the first level (see Fig. 5 and paragraphs 44-47, 53, and 56-60, signal line RYIBY is in a ready or H level while transmitting/receiving instructions or commands, such as rewrite data), notifying the second controller that the first controller is not ready for reception of the rewrite instruction or the rewrite data by changing the signal level of the report signal line from the first level to the second level and maintaining the report signal line at the second level while the data of the nonvolatile memory is rewritten by the first controller by the rewrite data transmitted from the second controller (see Fig. 5 and paragraphs 56-60, signal line RYIBY shifts to a busy or L level when executing rewriting), and notifying the second controller that the first controller is ready for reception of the rewrite data by changing the signal level of the report signal line from the second level to the first level, after the data of the nonvolatile memory has been rewritten by the first controller by the rewrite data transmitted from the second controller, wherein, in the rewrite mode, the data of the nonvolatile memory is repeatedly rewritten by the first

controller by the rewrite data transmitted from the second controller by alternately changing the signal level of the report signal line to the first level or to the second level, and (see Fig. 5 and paragraphs 44-47, 53, and 56-60, a signal line RYIBY that in a ready state is at a H level while executing instructions or commands, such as an instruction for rewrite mode shown by signal line IWE shifting to L and the signal line RYIBY shifts to a busy state or L level when executing rewrite processes and shifts to a ready or H level when available for data transmission/reception. As seen in figure 5 the "H" and "L" levels are maintained over a particular time interval and thereby synchronization by repeated notification is implied since the levels are maintained until a need for ready/busy state shifts are required for rewriting purposes).

Kazama discloses wherein there is a determination that an error has occurred if the signal level of the report signal line is not changed from the second level to the first level within a time period including time for communication between the first and second controllers and time for rewriting the data of the nonvolatile memory, and if it is determined that the error has occurred, the rewrite data is transmitted to the first controller by the second controller again (see Figs. 2, 5, and 10, column 2 lines 27-42, column 2 line 53-column 3 line 23, column 7 lines 18-39, and column 9 line 60-column 10 line 9, an error is detected if the signal stays high and is not set to low for longer than a prescribed time).

Regarding claim 11, Hanyu discloses an image forming apparatus including a first controller which controls an engine section for forming an image and a second controller which transmits image data to the first controller, wherein the engine section

includes a nonvolatile memory, and wherein the image forming apparatus is configured to operate in an image forming operation mode of forming an image with the engine section, or a rewrite mode of rewriting data of the nonvolatile memory, the apparatus comprising: means for, in the image forming operation mode, notifying the second controller of a condition change of the engine section by the first controller, (see Fig. 3 and column 8 line 59-column 9 line 60, input section **502** determines when an instruction to download a control program from a host computer is input via a control panel **303** and if so, the engine controller **301** terminates all operations of the printer engine and clears the flash EEPROM **301b** as to ready the flash EEPROM for download on the new control program. After the engine controller **301** completes the above tasks, it sends a demand for transmission, which is also seen as a notification that the engine controller is ready for control program download, to the printer controller **300**, which in turn acquires the control program from the host and then transfers the control program frame by frame to the engine controller **301** for writing of the program into memory, flash EEPROM **301b**), means for, in the image forming operation mode, transmitting a condition request instruction by the second controller to the first controller, in response to the notification of the condition change (see column 9 lines 4-10, in response to a download instruction the engine controller **301** terminates the operation of each component of the printer engine **302**), means for, in the rewrite mode, transmitting a rewrite instruction by the second controller to the first controller (see Fig. 3, column 4 lines 47-49, column 7 lines 30-34 and 48-54, column 8 lines 59-67, and column 9 lines 3-50, synchronization is established after an instruction to download a control program

is detected, then the engine controller requests transmission of a downloaded program from the printer controller, which in turn receives the downloaded program from the host computer, thereby instruction to retrieve the downloaded control program is initiated and in response the control program is transferred to the engine controller and the flash memory is rewritten with the new program), means for, in the rewrite mode, transmitting rewrite data by the second controller to the first controller, in response to the rewrite instruction, and means for, in the rewrite mode, rewriting the nonvolatile memory of the engine section by the first controller, by the rewrite data transmitted from the second controller (see Fig. 3, column 8 lines 1-8, and column 9 lines 51-65).

Hanyu does not disclose expressly signal lines for communication between the first controller and the second controller, wherein the signal lines include a report signal line and a data signal line, wherein a signal level of the report signal line is changed by the first controller, and wherein the data signal line is for transmitting data from the second controller to the first controller, notifying a condition change by changing a signal level of a report signal line from a first level to a second level, means for, in the image forming operation mode, transmitting a condition request instruction by the second controller to the first controller via a data signal line, while the signal level of the report signal line is at the second level, means for, in the image forming operation mode, transmitting condition information by the first controller to the second controller via the data signal line in response to the condition request instruction, after the signal level of the report signal line is returned to the first level, and means for, in the rewrite mode, transmitting a rewrite instruction by the second controller to the first controller via

the data signal line while the signal level of the report signal line is at the first level, means for, in the rewrite mode, transmitting rewrite data by the second controller to the first controller via the data signal line in response to the rewrite instruction, while the signal level of the report signal line is at the first level, means for, in rewrite mode, notifying the second controller that the first controller is not ready for reception of the rewrite instruction or the rewrite data by changing the signal level of the report signal line from the first level to the second level, and maintaining the report signal line at the second level while the data of the nonvolatile memory is rewritten by the first controller by the rewrite data transmitted from the second controller, and means for, in the rewrite mode, notifying the second controller that the first controller is ready for reception of the rewrite data by changing the signal level of the report signal line from the second level to the first level, after the data of the nonvolatile memory has been rewritten by the first controller by the rewrite data transmitted from the second controller, wherein, in the rewrite mode, the data of the nonvolatile memory is repeatedly rewritten by the first controller by the rewrite data transmitted from the second controller by alternately changing the signal level of the report signal line to the first level or to the second level.

Muto discloses notifying a condition change by changing a signal level of a report signal line from a first level to a second level (see paragraphs 58 and 69, signal line /CCRT is used to inform status change in the printer to print controller **213** and shifts from a high level to a low level when a change is detected in the printer), transmitting a condition request instruction by the second controller to the first controller via a data signal line, while the signal level of the report signal line is at the second level (see

paragraph 69, signal /CCRT stays at a low level while the printer controller **213** issues a command requesting the status), transmitting condition information by the first controller to the second controller via the data signal line in response to the condition request instruction, after the signal level of the report signal line is returned to the first level (see paragraphs 68, 71, and 73, after the change has cleared /CCRT returns to a high level and then the print controller **213**).

Ootani discloses in a rewrite mode transmitting an instruction by the second controller to the first controller via the data signal line, while the signal level of the report signal line is at the first level (see Fig. 5 and paragraphs 44-47, 53, and 56-60, signal line RYIBY is in a ready or H level while executing instructions or commands, such as an instruction for rewrite mode shown by signal line IWE shifting to L), transmitting rewrite data by the second controller to the first controller via the data signal line in response to the rewrite instruction, while the signal level of the report signal line is at the first level (see Fig. 5 and paragraphs 44-47, 53, and 56-60, signal line RYIBY is in a ready or H level while transmitting/receiving instructions or commands, such as rewrite data), notifying the second controller that the first controller is not ready for reception of the rewrite instruction or the rewrite data by changing the signal level of the report signal line from the first level to the second level (see Fig. 5 and paragraphs 56-60, signal line RYIBY shifts to a busy or L level when executing rewriting), and maintaining the report signal line at the second level while the data of the nonvolatile memory is rewritten by the first controller by the rewrite data transmitted from the second controller, and means for, in the rewrite mode, notifying the second controller that the first controller is ready

for reception of the rewrite data by changing the signal level of the report signal line from the second level to the first level, after the data of the nonvolatile memory has been rewritten by the first controller by the rewrite data transmitted from the second controller, wherein, in the rewrite mode, the data of the nonvolatile memory is repeatedly rewritten by the first controller by the rewrite data transmitted from the second controller by alternately changing the signal level of the report signal line to the first level or to the second level (see Fig. 5 and paragraphs 44-47, 53, and 56-60, a signal line RYIBY that in a ready state is at a H level while executing instructions or commands, such as an instruction for rewrite mode shown by signal line IWE shifting to L and the signal line RYIBY shifts to a busy state or L level when executing rewrite processes and shifts to a ready or H level when available for data transmission/reception. As seen in figure 5 the "H" and "L" levels are maintained over a particular time interval and thereby synchronization by repeated notification is implied since the levels are maintained until a need for ready/busy state shifts are required for rewriting purposes).

Kazama discloses wherein it is determined that an error has occurred if the signal level of the report signal line is not changed from the second level to the first level within a time period including time for communication between the first and second controllers and time for rewriting the data of the nonvolatile memory, and if it is determined that the error has occurred, the rewrite data is transmitted to the first controller by the second controller again (see Figs. 2, 5, and 10, column 2 lines 27-42, column 2 line 53-column 3 line 23, column 7 lines 18-39, and column 9 line 60-column

10 line 9, an error is detected if the signal stays high and is not set to low for longer than a prescribed time).

Hanyu, Muto, Ootani & Kazama are combinable because they are from the same field of endeavor, writing of data via signal lines.

At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the detection of an error by determining if a predetermined amount of time has passed since a signal line change, as described by Kazama, and much like a watchdog time which is well known and commonly used in the art, the ready and busy levels of a signal line for rewrite processing, as described by Ootani, and the use of signal and reporting lines that indicate condition change and status information, as described by Muto, and which is well known and commonly used in the art, with the system of Hanyu.

The suggestion/motivation for doing so would have been to decrease the possibility of errors during rewriting of data by notifying status and condition changes and busy and ready states to ensure proper writing of data is executed.

Therefore, it would have been obvious to combine Muto, Ootani, and Kazama with Hanyu to obtain the invention as specified in claims 1 and 11.

Regarding claim 3, Hanyu further discloses in the image forming operation mode, transmitting a condition of the engine section by the first controller to the second controller (see column 9 lines 4-10 and 32-37, in response to a download instruction the engine controller 301 terminates the operation of each component of the printer engine

302. After the engine controller **301** completes the above tasks, it sends a demand for transmission, which is also seen as a notification that the engine controller is ready for control program download, to the printer controller **300**, which in turn acquires the control program from the host and then transfers the control program frame by frame to the engine controller **301** for writing of the program into memory, flash EEPROM **301b**). Muto further discloses in the rewrite mode, transmitting a condition of the first controller by the first controller to the second controller via the data signal line, after the signal level is changed from the first level to the second level (see paragraphs 68, 71 and 73, when the printer controller **213** issues a command to the engine controller **202**, the /CBSY signal is shifted to a low level state, at which time the status of the engine is transmitted, and then the /CBSY signal returns to the high level and the printer controller **213** continues to request printing).

Regarding claim 4, Hanyu further discloses wherein a condition of the first controller is one of a data transfer error, an erase or rewrite operation result of the nonvolatile memory, or an end of the rewrite operation of the nonvolatile memory (see column 5 lines 14-20 and column 9 line 66-column 10 line 5).

Regarding claim 8, Hanyu further discloses wherein the rewrite data is a control program code data (see column 3 lines 50-54, column 3 line 66-column 4 line 3, and column 4 lines 47-49).

Regarding claim 10, Hanyu further discloses wherein the nonvolatile memory is a flash memory (see Fig. 3 **301b**).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mark R. Milia whose telephone number is (571)272-7408. The examiner can normally be reached M-F 8:00am-4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Moore can be reached at (571) 272-7437. The fax number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Mark R. Milia
Examiner
Art Unit 2625

/Mark R. Milia/
Examiner, Art Unit 2625

/David K Moore/

Art Unit: 2625

Supervisory Patent Examiner, Art Unit 2625